



## UNITED STATES PATENT AND TRADEMARK OFFICE

COMMISSIONER FOR PATENTS  
UNITED STATES PATENT AND TRADEMARK OFFICE  
WASHINGTON, D.C. 20231  
www.uspto.gov



Bib Data Sheet

CONFIRMATION NO. 3745

|                                    |   |                     |                               |  |
|------------------------------------|---|---------------------|-------------------------------|--|
| <b>SERIAL NUMBER</b><br>09/891,925 | <b>FILING DATE</b><br>06/27/2001<br><b>RULE</b> | <b>CLASS</b><br>257 | <b>GROUP ART UNIT</b><br>2811 | <b>ATTORNEY DOCKET NO.</b><br>198786US2 RE |
|------------------------------------|---|---------------------|-------------------------------|--|

**APPLICANTS**

Mitsuhiro Yano, Fukuoka-shi, JAPAN;  
Kouichi Mochizuki, Fukuoka-shi, JAPAN;

**\*\* CONTINUING DATA \*\*\*\*\***

THIS APPLICATION IS A REI OF 08/432,812 05/02/1995 PAT 5,945,692

SWC

**\*\* FOREIGN APPLICATIONS \*\*\*\*\***

JAPAN 6-118386(P) 05/31/1994

SWC

**IF REQUIRED, FOREIGN FILING LICENSE GRANTED**

\*\* 08/14/2001

|   |                                  |                             |                           |                                |
|---|----------------------------------|-----------------------------|---------------------------|--------------------------------|
| Foreign Priority claimed<br><input checked="" type="checkbox"/> yes <input type="checkbox"/> no   | <b>STATE OR COUNTRY</b><br>JAPAN | <b>SHEETS DRAWING</b><br>11 | <b>TOTAL CLAIMS</b><br>22 | <b>INDEPENDENT CLAIMS</b><br>2 |
| 35 USC 119 (a-d) conditions met<br><input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance |                                  |                             |                           |                                |
| Verified and Acknowledged<br>Examiner's Signature <u>SWC</u> Initials   |                                  |                             |                           |                                |

**ADDRESS**

22850

**TITLE**

Semiconductor device and method of fabricating same

|                                   |   |  |
|-----------------------------------|---|--|
| <b>FILING FEE RECEIVED</b><br>710 | FEES: Authority has been given in Paper<br>No. _____ to charge/credit DEPOSIT ACCOUNT<br>No. _____ for following: | <input type="checkbox"/> All Fees                              |
|                                   |   | <input type="checkbox"/> 1.16 Fees ( Filing )                  |
|                                   |   | <input type="checkbox"/> 1.17 Fees ( Processing Ext. of time ) |
|                                   |   | <input type="checkbox"/> 1.18 Fees ( Issue )                   |
|                                   |   | <input type="checkbox"/> Other _____                           |
|                                   |   | <input type="checkbox"/> Credit                                |



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| Foreign Priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no<br>35 USC 119 (a-d) conditions <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after<br>met Allowance<br>Verified and Acknowledged |   | <b>STATE OR COUNTRY</b><br>JAPAN | <b>SHEETS DRAWING</b><br>11   | <b>TOTAL CLAIMS</b><br>11                  | <b>INDEPENDENT CLAIMS</b><br>2 |
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| <b>TITLE</b><br>Semiconductor device and method of fabricating same  |   |                                  |   |  |                                |
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